Patent
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT	:	Masao Watari et al.
SERIAL NO.	:	10/542,103
FILING DATE	:	July 12, 2005
FOR	:	Operation Device, Operation Device Designing Method, and Logic Circuit Designing Method
EXAMINER	:	Joseph D. Torres
GROUP ART UNIT	:	2112
CONFIRMATION NO.	:	1068

6 Pages
Via EFS-Web
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Commissioner for Patents
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ELECTION WITH AMENDMENT

Sir:

In response to the Restriction Requirement dated February 17, 2009, please amend the above identified application as follows: